

10024119_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 10024119 on March 10, 2003

17	326/86	(4 OR, 13 XR)
	Class 326 :	ELECTRONIC DIGITAL LOGIC CIRCUITRY
	326/62	INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT, ETC.)
	326/82	.Current driving (e.g., fan in/out, off chip driving, etc.)
	326/83	..Field-effect transistor
	326/86	...Bus driving
14	326/30	(8 OR, 6 XR)
	Class 326 :	ELECTRONIC DIGITAL LOGIC CIRCUITRY
	326/21	SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
	326/30	.Bus or line termination (e.g., clamping, impedance matching, etc.)
8	326/83	(0 OR, 8 XR)
	Class 326 :	ELECTRONIC DIGITAL LOGIC CIRCUITRY
	326/62	INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT, ETC.)
	326/82	.Current driving (e.g., fan in/out, off chip driving, etc.)
	326/83	..Field-effect transistor
7	326/21	(0 OR, 7 XR)
	Class 326 :	ELECTRONIC DIGITAL LOGIC CIRCUITRY
	326/21	SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
7	327/108	(4 OR, 3 XR)
	Class 327 :	MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
	327/100	SIGNAL CONVERTING, SHAPING, OR GENERATING
	327/108	.Current driver
5	326/26	(1 OR, 4 XR)
	Class 326 :	ELECTRONIC DIGITAL LOGIC CIRCUITRY
	326/21	SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
	326/26	.Output switching noise reduction
5	326/27	(1 OR, 4 XR)
	Class 326 :	ELECTRONIC DIGITAL LOGIC CIRCUITRY
	326/21	SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
	326/26	.Output switching noise reduction
	326/27	..With field effect-transistor

10024119_CLSTITLES

4 326/17 (0 OR, 4 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/17 ACCELERATING SWITCHING

4 326/57 (0 OR, 4 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/56 TRI-STATE (I.E., HIGH IMPEDANCE AS THIRD STATE
)
 326/57 .With field effect-transistor

4 326/87 (1 OR, 3 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/62 INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT,
 ETC.)
 326/82 .Current driving (e.g., fan in/out, off chip
 driving, etc.)
 326/83 ..Field-effect transistor
 326/87 ...Having plural output pull-up or pull-down
 transistors

4 327/112 (1 OR, 3 XR)
 Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
 DEVICES, CIRCUITS, AND SYSTEMS
 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
 327/108 .Current driver
 327/111 ..Having capacitive load
 327/112 ...Push-pull

3 326/58 (2 OR, 1 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/56 TRI-STATE (I.E., HIGH IMPEDANCE AS THIRD STATE
)
 326/57 .With field effect-transistor
 326/58 ..Complementary FET`s

3 326/82 (2 OR, 1 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/62 INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT,
 ETC.)
 326/82 .Current driving (e.g., fan in/out, off chip
 driving, etc.)

3 327/310 (0 OR, 3 XR)
 Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
 DEVICES, CIRCUITS, AND SYSTEMS

10024119_CLSTITLES

327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
 327/306 .Amplitude control
 327/309 ..By limiting, clipping, or clamping
 327/310 ...Transient or signal noise reduction

3 365/189.11 (2 OR, 1 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/189.01 READ/WRITE CIRCUIT
 365/189.11 .Including level shift or pull-up circuit

2 326/121 (0 OR, 2 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/104 FUNCTION OF AND, OR, NAND, NOR, or NOT
 326/112 .Field-effect transistor (e.g., JFET, etc.)
 326/119 ..MOSFET (i.e., metal-oxide semiconductor
 field-effect transistor)
 326/121 ...CMOS

2 326/33 (0 OR, 2 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/21 SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
 326/31 .Signal level or switching threshold
 stabilization
 326/33 ..Bias or power supply level stabilization

2 326/56 (1 OR, 1 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/56 TRI-STATE (I.E., HIGH IMPEDANCE AS THIRD STATE

2 326/68 (1 OR, 1 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/62 INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT,
 ETC.)
 326/63 .Logic level shifting (i.e., interface between
 devices of different logic families)
 326/68 ..Field-effect transistor (e.g., JFET, MOSFET,
 etc.)

2 326/80 (0 OR, 2 XR)
 Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
 326/62 INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT,
 ETC.)
 326/80 .Supply voltage level shifting (i.e., interfac

e
 different between devices of a same logic family with

10024119_CLSTITLES
operating voltage levels)

2 326/90 (0 OR, 2 XR)
Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
326/62 INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT,
ETC.)
326/82 .Current driving (e.g., fan in/out, off chip
driving, etc.)
326/89 ..Bipolar transistor
326/90 ...Bus driving

2 326/93 (1 OR, 1 XR)
Class 326 : ELECTRONIC DIGITAL LOGIC CIRCUITRY
326/93 CLOCKING OR SYNCHRONIZING OF LOGIC STAGES OR
GATES

2 327/333 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
327/306 .Amplitude control
327/333 ..Interstage coupling (e.g., level shift, etc.)

2 327/379 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/365 GATING (I.E., SWITCHING INPUT TO OUTPUT)
327/379 .Signal transmission integrity or spurious
noise override

2 327/382 (2 OR, 0 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/365 GATING (I.E., SWITCHING INPUT TO OUTPUT)
327/379 .Signal transmission integrity or spurious
noise override
327/382 ..Parasitic prevention or compensation (e.g.,
parasitic capacitance, etc.)

2 327/391 (0 OR, 2 XR)
Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
DEVICES, CIRCUITS, AND SYSTEMS
327/365 GATING (I.E., SWITCHING INPUT TO OUTPUT)
327/379 .Signal transmission integrity or spurious
noise override
327/389 ..Insulated gate FET (e.g., MOSFET, etc.)

10024119_CLSTITLES

327/391 ...Complementary metal-oxide semiconductor
 (CMOS)

2 327/427 (0 OR, 2 XR)
 Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
 DEVICES, CIRCUITS, AND SYSTEMS
 327/365 GATING (I.E., SWITCHING INPUT TO OUTPUT)
 327/419 .Utilizing three or more electrode solid-state
 device
 327/427 ..Field-effect transistor

2 365/189.05 (0 OR, 2 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/189.01 READ/WRITE CIRCUIT
 365/189.05 .Having particular data buffer or latch

2 365/203 (0 OR, 2 XR)
 Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL
 365/189.01 READ/WRITE CIRCUIT
 365/203 .Precharge

2 710/107 (0 OR, 2 XR)
 Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS
 TRANSACTION PROCESSING)
 710/107 .Bus access regulation